

# Power Supply - part 1

## 1. Scope

- Build various type of voltage regulator
  - Zener diode,
  - Zener diode+ transistor (series),
  - Buck Switching Regulator,
  - Boost Switching Regulator,
  - Buck-Boost Switching Regulator,
  - Flyback Switching Regulator.
- Calculate circuit parameters to achieve reference voltage.

## 2. Zener Diode Voltage Regulator

Run Matlab and load file named LAB4.slx. Uncomment block called *Zener Diode*.

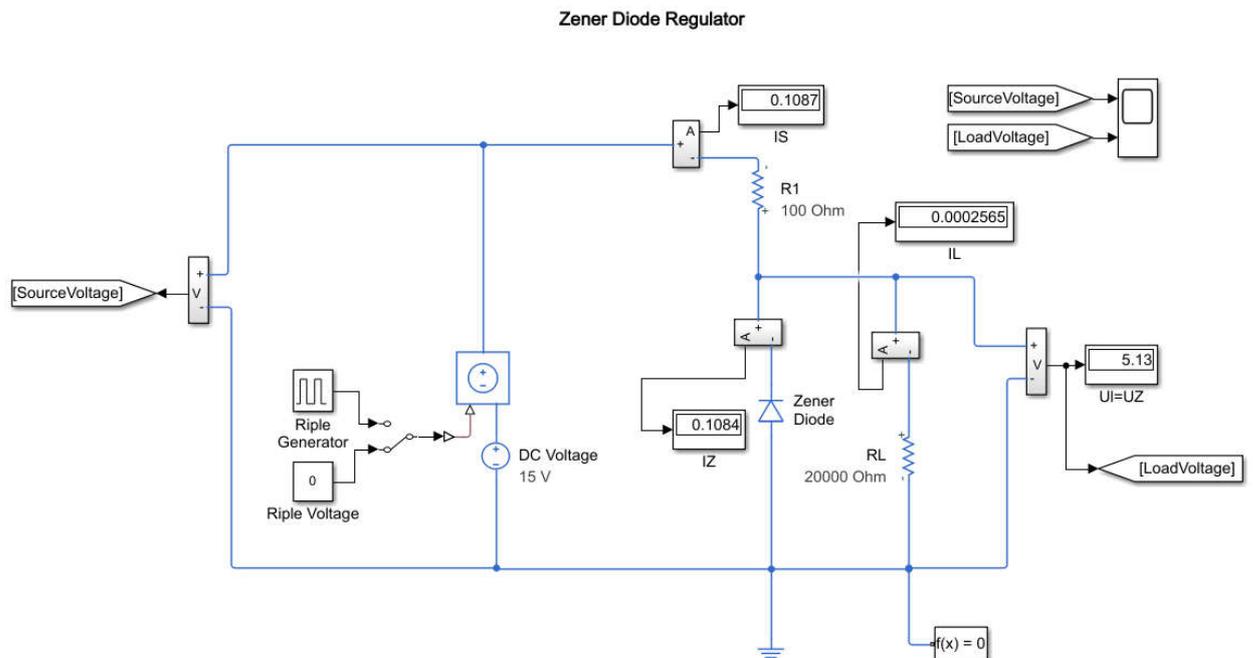


Fig. 1 Schematic of the Zener Diode Regulator

Diode parameters:  $I_{Zmax} = \dots$ ,  $U_Z = \dots$

Assume Zener diode works correctly with the current  $I_Z$  in range 10%-100%  $I_{Zmax}$ .

- calculate value of the resistor  $R_1$  to be sure, that Zener diode works correctly.
- calculate range of load resistance that could be supplied from the Zener regulator,
- calculate power dissipated on load and  $R_1$ ,
- plot in the Fig. 2 the characteristic  $U_L = f(I_L)$  with constant DC Voltage. Change  $I_L$  from 0 up to max,
- plot in the Fig. 3 the characteristic  $U_L = f(U_S)$  with constant  $I_L$ . Change  $U_S$  (DC voltage)  $I_L$  from 10 up to 20V, modify  $R_L$  to maintain constant  $I_L$ .
- change the switch to enable pulse generator. Check how pulse in the source voltage are transmitted to the load voltage. Plot results in Fig. 4.

Fig. 2  $U_L=f(I_L)$  with constant DC Voltage

Fig. 3  $U_L=f(U_S)$  with constant  $I_L$

Fig. 4 Source Voltage, Load Voltage

Comments:

### 3. Zener Diode with Transistor (Series Voltage Regulator)

Run Matlab and load file named LAB4.slx. Uncomment block called *Zener Diode with Transistor (Series)*

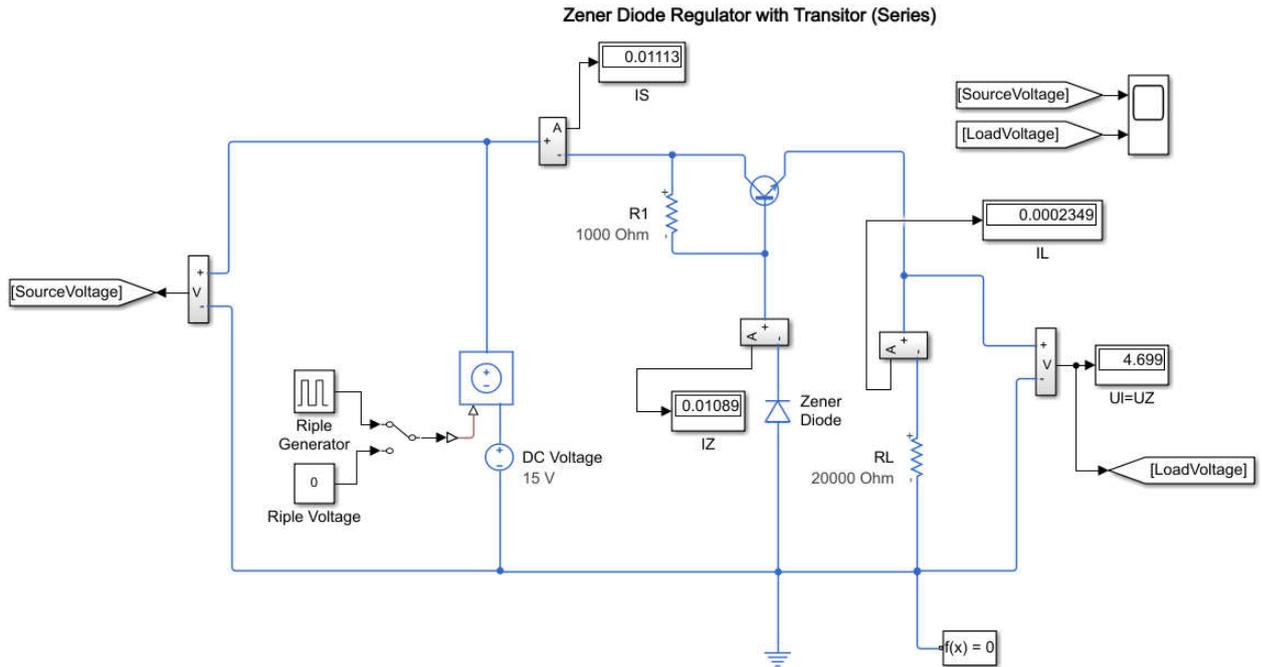


Fig. 5 Schematic of the Zener Diode Regulator with Transistor (series)

Modify the Zener Diode parameters (Reverse breakdown voltage) to get the load voltage equal 5.1 V.  $I_{Zmax}$  remain unchanged.

- calculate value of the resistor  $R_1$  to be sure, that Zener diode works correctly.
- determine range of load resistance that could be supplied from the regulator (load voltage is about. 5.1V),
- calculate power dissipated on load,  $R_1$  and transistor (modify schematic to measure transistor voltage  $U_{ce}$ ),
- plot in the Fig. 6 the characteristic  $U_L=f(I_L)$  with constant DC Voltage. Change  $I_L$  from 0 up to max,
- plot in the Fig. 7 the characteristic  $U_L=f(U_S)$  with constant  $I_L$ . Change  $U_S$  (DC voltage)  $I_L$  from 10 up to 20V, modify  $R_L$  to maintain constant  $I_L$ .
- change the switch to enable pulse generator. Check how pulse in the source voltage are transmitted to the load voltage. Plot results in Fig. 8.

Fig. 6  $U_L=f(I_L)$  with constant DC Voltage

Fig. 7  $U_L=f(U_S)$  with constant  $I_L$

Fig. 8 Source Voltage, Load Voltage

Comments:

Compare results for both type of regulators: